



09/22/00

	
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

## U.S. UTILITY Patent Application

<p>    <b>SCANNED</b> </p>	<p> <b>O.I.P.E.</b> </p>	<p> <b>PATENT DATE</b> </p>
<p>    <b>Q.A.</b> </p>		

*APPLICATION NO. 09/668320	CONT/PRIOR	CLASS 703	SUBCLASS 2	ART UNIT 2123	EXAMINER FREJD
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## APPLICANTS

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TIME

Method and system for determining an interconnect delay utilizing an effective capacitance metric (ECM) signal delay model

PTD-2040  
12/89[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>  	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____ _____	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	
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